Jaemin Kim

Curriculum Vitae

CONTACT INFORMATION

School of Electrical and Electronic Engineering
Yonsei University
Engineering Hall #3 C407
50 Yonsei-Ro Seodaemun-gu
Seoul, South Korea, 03722

jaemin.kim@yonsei.ac.kr
jaemin@corelab.or.kr
https://jae-min-kim.github.io

EDUCATION

Yonsei University, Seoul, Republic of Korea
M.S./Ph.D. Student, March 2026 to Present
   Advisor: Prof. Hanjun Kim
   Research Project: "Hardware Accelerator Design for Fully Homomorphic Encryption"

Yonsei University, Seoul, Republic of Korea
Bachelor of Engineering in Electrical and Electronic Engineering, March 2020 to February 2026

EXPERIENCE

Research Assistant, March 2026 to Present
Compiler Research Laboratory (Corelab), *Yonsei University*, Seoul, Republic of Korea

- Hardware accelerator design for homomorphic encryption

Undergraduate Research Assistant, July 2025 to February 2026
Compiler Research Laboratory (Corelab), *Yonsei University*, Seoul, Republic of Korea

- A compiler for mapping high-level code to low-level RISC-V-based accelerator instructions

RECOGNITION

- Undergraduate Silver Prize, Code Generation and Optimization Student Research Competition, Feb 2026

PUBLICATIONS