

Haeun Jeong

Curriculum Vitae

CONTACT INFORMATION

School of Electrical and Electronic Engineering
Yonsei University
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EDUCATION

Yonsei University, Seoul, Republic of Korea
M.S./Ph.D. Student, February 2024 to Present
Advisor: Prof. Hanjun Kim
Research Project: DNN Compiler[“]

Seoul National University of Science and Technology, Seoul, Republic of Korea
Bachelor of Engineering in Electronic and IT Media Engineering, March 2019 to February 2024

EXPERIENCE

Research Assistant, March 2024 to Present
Compiler Optimization Research Laboratory (CORELAB), *Yonsei University*, Seoul, Republic of Korea

- DNN Compiler for On-device AI

Internship Student, February 2023 to September 2023
Artificial Intelligence and Robotics Institute, Korea Institute of Science and Technology, Seoul, Republic of Korea

- Deep Learning-based Image Processing for Visual Servo Control of Robot Arms

RECOGNITION

- Academic Excellence Award, Honors, spring 2022

PUBLICATIONS

REFEREED CONFERENCE PUBLICATIONS

- [1] Seungbin Song, Ju Min Lee, Haeun Jeong, Hyunho Kwon, Shinnung Jeong, Jaeho Lee, and Hanjun Kim, “TeMCO: Tensor Memory Compiler Optimization across Tensor Decompositions in Deep Learning Inference,” in *Proceedings of the 53rd International Conference on Parallel Processing (ICPP)*, August 2024.

PATENTS

- [2] Hanjun Kim, Seungbin Song, Hyunho Kwon, Jumin Lee, Haeun Jeong, and Jaeho Lee, “Compile Method and Apparatus for Neural Network,” KR Patent App. 10-2024-0088419, July 2024.