Chan Lee

Curriculum Vitae

CONTACT INFORMATION

School of Electrical and Electronic Engineering
Yonsei University
Engineering Hall #3 C407
50 Yonsei-Ro Seodaemun-gu
Seoul, South Korea, 03722

chan.lee@yonsei.ac.kr
chan@corelab.or.kr
http://www.corelab.or.kr/~chan

EDUCATION

Yonsei University, Seoul, Republic of Korea
M.S./Ph.D. Student, September 2024 to Present
  Advisor: Prof. Hanjun Kim
  Research Project: Processing In Memory Compiler""

Yonsei University, Seoul, Republic of Korea
Bachelor of Engineering in Electrical and Electronic Engineering, March 2018 to February 2024
  Thesis: "Exploring Optimization techniques for Processing-in-Memory Architecture: A Case Study on UPMEM"

EXPERIENCE

Research Assistant, March 2024 to Present
Compiler Research Laboratory (Corelab), *Yonsei University*, Seoul, Republic of Korea

- Processing In Memory Compiler

Undergraduate Research Assistant, January 2023 to February 2024
Compiler Research Laboratory (Corelab), *Yonsei University*, Seoul, Republic of Korea

- Processing In Memory Compiler

RECOGNITION

- Academic Excellence Award, Honors, spring 2023

TEACHING

- EEE2113: Electrical and Electronic Engineering 101, Yonsei University
  Teaching Assistant, Yonsei University, Spring 2024
  (Teaching Python Programming))

PUBLICATIONS